Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	62	(DEVINS-ROBERT-J DEVINS-ROBERT-JAMES FERRO-PAUL-G FERRO-PAUL-J KELLER-EMORY-D MILTON-DAVID MILTON-DAVIDSON MILTON-DAVID-W MILTON-DAVID-WILLS).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR .	ON	2005/08/16 18:41
L12	24	L11 and (soc)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:41
L13	13	L12 and (external with bus)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:41
L14	13	L13 and (test or bench or testbench)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:41
L19	1223	((external with bus with interface) or (ebiu)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:47
L20	92	((external adj bus adj interface) or (ebiu)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:47
L22	8	I20 and ((system with chip) or (soc)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:48
L23	1	"6487699".pn. and (connection adj "107")	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 19:46
S1	168489	((system adj on adj a adj chip) or (soc))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:23
S2	112	((external adj bus adj interface adj unit) or (ebiu))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:24
S3	7	S1 and S2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR -	ON	2005/08/15 19:30

S4	5	S3 and (test or bench or test\$bench)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:42
S5	0	US20000602422	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:42
S6	0	US2000/0602422	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON .	2005/08/15 19:42
S7	0	2000/0602422	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:43
S8	1	"0602422"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:42
S9	0	MEIYAPPAN and SUBRAMANIAN and VARAPRASAD and VAJJHALA	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:43
S10	4284	MEIYAPPAN SUBRAMANIAN VARAPRASAD VAJJHALA	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:43
S11	12	MEIYAPPAN-SUBRAMANIAN MEIYAPPAN-SUBRAMANIAN-S	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:45
S12	0	"VERIFICATION OF INTEGRATED CIRCUIT DESIGN".ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:44
S13	0	S11 and (VERIFICATION).ti.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON .	2005/08/15 19:46
S14	62	(DEVINS-ROBERT-J DEVINS-ROBERT-JAMES FERRO-PAUL-G FERRO-PAUL-J KELLER-EMORY-D MILTON-DAVID MILTON-DAVIDSON MILTON-DAVID-W MILTON-DAVID-WILLS).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:48
S15	24	S14 and (soc)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:48

S16	1	S15 and ebiu	US-PGPUB;	OR	ON	2005/08/15 19:49
310	•		USPAT; EPO; JPO; IBM_TDB			2003/00/13 13:13
S17	13	S15 and (external with bus)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:55
S19	6	S17 and ((test adj bench) or testbench)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:55
S20	6	S19 and (verify or verification or verifying)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 19:56
S21	969	S1 and ((external with bus) or (ebiu))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/16 18:46
S22	34	S21 and ((test adj bench) or testbench)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 20:01
S23	34	S22 and (verify or verification or verifying)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 20:01
S24	28	S23 not S20	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 20:02
S25	555	S1 and ((external near3 bus) or (ebiu))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 20:01
S26	. 12	S25 and ((test adj bench) or testbench)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 20:01
S27	12	S26 and (verify or verification or verifying)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR .	ON	2005/08/15 20:01
S28	6	S27 not S20	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/08/15 20:02